AMENDMENT TRANSMITTAL LETTER NOV 2 6 2004						Attorney Docket No:	
Application Serial Number:		Filing Date: 06/20/2003		Examiner: Tran, Tan N.		N18347102E Group Art Unit: 2826	
Invention: Semiconductor Integrated Circuit Including an Inductor and Method of Manufacturing the Same							
TO THE COMMISSIONER OF PATENTS AND TRADEMARKS: Transmitted herewith is an amendment in the above-identified application. The fee has been calculated as shown below.							
CLAIMS AS AMENDED							
	CLAIMS REMAINING AFTER AMENDMENT		HIGHEST NUMBER PREVIOUSLY PAID FOR	NO. OF EXTRA CLAIMS PRESENT	RATE	ADDITIONAL FEE	
TOTAL CLAIMS	13	MINUS	35	0	\$18	\$ 0.00	
INDEP. CLAIMS	5	MINUS	7	0	\$88	\$ 0.00	
Petition is hereby made under 37 CFR 1.136(a) to extend the time for response to the Office Action of to and through, comprising an extension of the shortened statutory period of: one month (\$110)three months (\$950) two months (\$420)four months (\$1480)							
Small entity status of this application under 37 CFR 1.9 and 1.27 has been established by a verified statement previously submitted. A verified statement to establish small entity status under 37 CFR 1.9 and 1.27 is enclosed. A check in the amount of \$ 0.00 is attached. Charge \$ to Deposit Account X No additional fee is required. It is believed that the indicated items are timely filed. However, in the event an extension of time is required, please consider this a petition for extension of time under 37 C.F.R. §1.136(a). Please charge any fees that may be required to Deposit Account No. 50-2087.							
November 22, 2004 date Darryl Walker Reg. No. 43,232 I hereby certify that this correspondence and all correspondence identified as accompanying this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313 on							

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

	Applicant(s): Furumiya, Masayuki						
PE 3	Serial No.: 10/600,104	Group Art Unit: 2826					
ON 2 6 2004 E	Filed: June 20, 2003	Danis Trans Trans N					
MT & TRADERAS	Title (Currently Amended): Semiconductor Integrated Circuit Including an Inductor and Method of Manufacturing the Same	Examiner: Tran, Tan N.					
	Attorney Docket No.: N18347102E						
	RESPONSE TO OFFICE ACTION						
5	Mail Stop Amendment Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450						
10	A. Introductory Comments The following is submitted in response to the Office Action dated August 26, 2004, and is						
	currently due November 26, 2004.						
15							
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	I hereby certify that this correspondence is being	.R. §1.8					
25	[] transmitted via facsimile to the United States Patent and Trademark Office to fax number:						
	[X] deposited with the United States Postal Service as first cla P.O. Box 1450, Alexandria, VA 22313-1450.	ss mail in an envelope addressed to: Commissioner for Patents,					
30	Date of Deposit: November 22, 2004						
35	Typed/Printed Name: <u>Darryl G. Walker</u> Signature: <u>Uul</u>						